(OAP1122B-0216-EC)

MFG Test Plan

OAP1122B-0216-EC

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Revision History

Rev.	Date	Author	Revision Description
0Y1.00	2016/07/18	Maxime	1st Release
0Y1.01	2016/08/01	Maxime	Modify test equipment in FT2

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1. Overview

1.1 Purpose

This document describes the manufacturing test plan for the OAP1122B-0216-EC product. It also identifies design requirements that need to be fulfilled for successful implementation of this test plan. Target audience for this document is the EC operations & design team and contract manufacturing personnel.

2. Test Process

2.1 Goals

Follow up the Test Plan and sync with all member include MFG. Development team and customer side can well understanding and follow up to maintain the good quality for shipment.

2.2 Key Issues

2.3 Test Process Flow

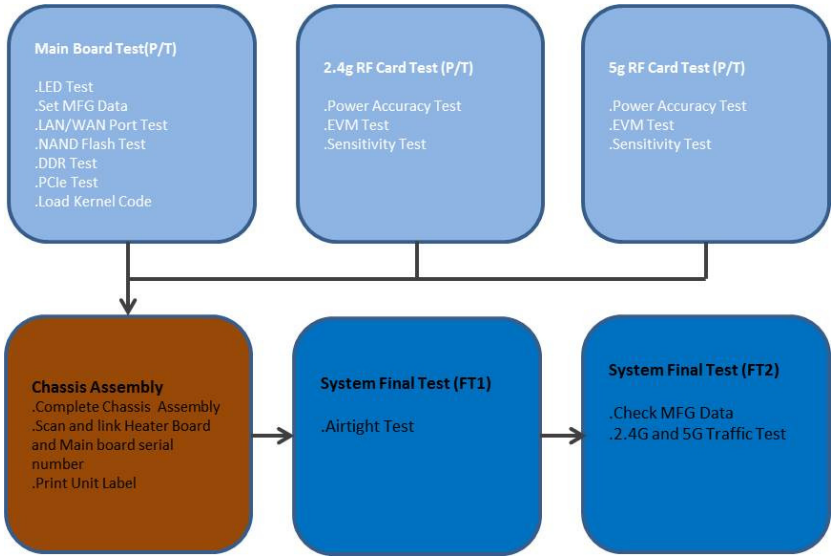


Figure 1 Test Process Flow

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2.3.1 2.4g RF Card Configuration Testing (P/T)

2.3.1.1 Test Environment Setup

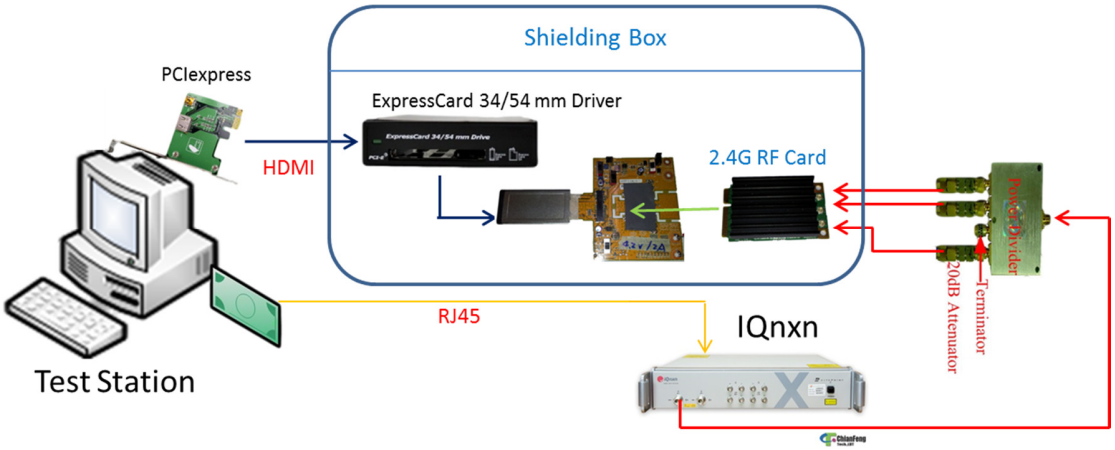


Figure 2 2G RF P/T Testing Configuration

2.3.1.2 Hardware Requirement

1	Pentium PC (or above) running WinXP	x 1
2	PCI Express with HDMI cable	x 1
3	ExpressCard 34/54 mm Drive (or PCIE extended card)	x 1
4	Express Card to PCMCIA (or PCIE extended card)	x 1
5	Power Divider	x 1
6	20db Attenuator	x 3
7	IQnxn	x 1
8	Shielding Box	x 1

Table 1 2G RF Testing HW Requirement

2.3.1.3 Software Requirement

1	Microsoft OS Windows XP or above	
2	ActiveTCL: Tcl v8.5.15	
3	MATLAB(R) Compiler Runtime 7.9	
4	Microsoft Visual C++ 2005 Redistributable	
5	Microsoft Visual C++ 2008 Redistributable	

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6	IQ201X Applications	

Table 2 2G RF Testing Software Requirement

2.3.1.4 Test Requirements

Run requested Test Items without any fail condition.

2.3.1.5 Test Log



OAP1122B_RF2G_Test_PT_AE37007750_20150415_182425_PASS_N_1.cap

2.3.1.6 Test Estimated Cycle Test

Station Name	Max of Units	Estimation (Second (s) of Time)
P/T	1	85 (s)

Notice :

(1) This is not really cycle time of the DUT run, it just only references for engineering and manufacturing.

(2) The program does not comprise the factitious manipulation time in manufacturing.

(3) ACCTON IE has controlled evaluation the real testing cycle time in manufacturing.

2.3.2 5g RF Card Configuration Testing (P/T)

2.3.2.1 Test Environment Setup

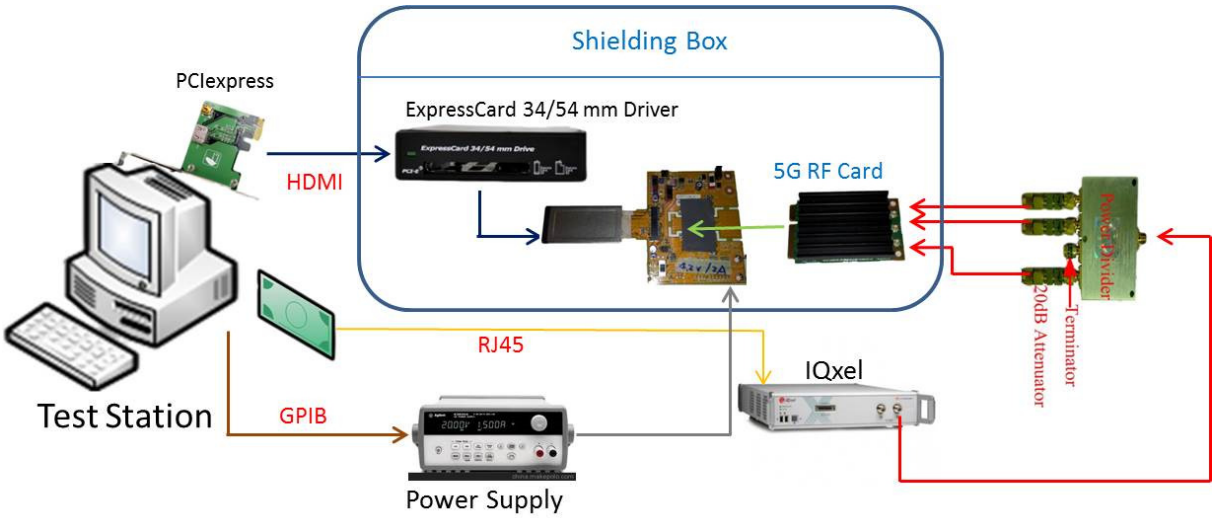


Figure 3 5G RF P/T Testing Configuration

2.3.2.2 Hardware Requirement

1	Pentium PC (or above) running WinXP	x 1
2	PCI Express with HDMI cable	x 1
3	ExpressCard 34/54 mm Drive (or PCIE extended card)	x 1
4	Express Card to PCMCIA (or PCIE extended card)	x 1
5	Power Divider	x 1
6	20db Attenuator	x 3
7	IQxel	x 1
8	Shielding Box	x 1
9	Power supply	x 1
10	USB to GPIB	x 1

Table 3 5G RF Testing HW Requirement

2.3.2.3 Software Requirement

1	Microsoft OS Windows XP or above	

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2	ActiveTCL: Tcl v8.5.15	
3	MATLAB(R) Compiler Runtime 7.9	
4	Microsoft Visual C++ 2005 Redistributable	
5	Microsoft Visual C++ 2008 Redistributable	
6	IQ201X Applications	

Table 4 5G RF Testing Software Requirement

2.3.2.4 Test Requirements

Run requested Test Items without any fail condition.

2.3.2.5 Test Log



OAP1122B_RF5G_Test_PT_AE39001366_20150416_095053_PASS_N_1.cap

2.3.2.6 Test Estimated Cycle Test

Station Name	Max of Units	Estimation (Second (s) of Time)
P/T	1	65 (s)

Notice :

(1) This is not really cycle time of the DUT run, it just only references for engineering and manufacturing.

(2) The program does not comprise the factitious manipulation time in manufacturing.

(3) ACCTON IE has controlled evaluation the real testing cycle time in manufacturing.

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2.3.3 Board Level Functional Test (P/T)

2.3.3.1 Test Environment Setup

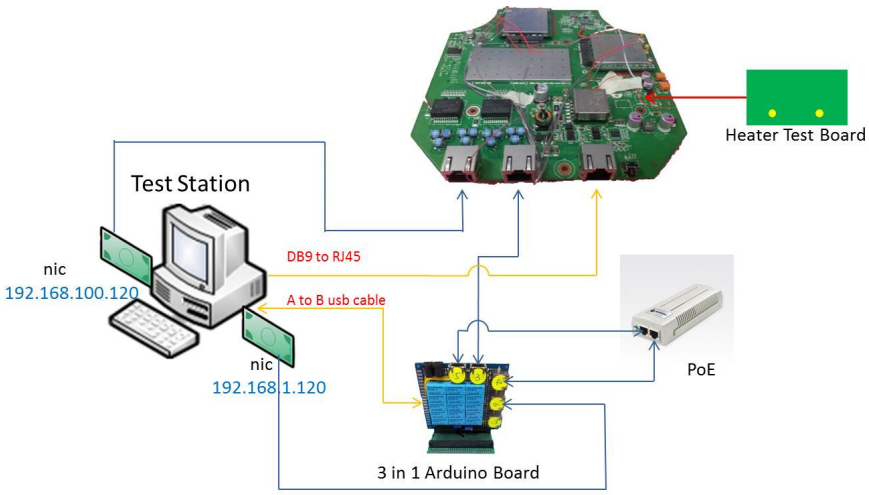


Figure 4 P/T Main Board Test Setup Configuration

2.3.3.2 Hardware Requirement

1	Pentium PC (or above) running WinXP	x 1
2	RS232 Console cable	x 1
3	PoE injector	x 1
4	RJ45 cable	x 3
5	3 in 1 Arduino Board(optional)	x 1
6	A to B USB cable(optional)	x 1
7	Heater Test Board	x 1

Table 5 P/T Main Board Test Hardware Requirement

2.3.3.3 Software Requirement

1	Microsoft OS Windows XP	
2	ActiveTCL: Tcl v8.5.15	

Table 6 P/T Main Board Test Software Requirement

2.3.3.4 Test Requirements (PASS/FAIL Criteria)

Run requested Test Items without any fail condition.

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2.3.3.4.1. Check Heater

Check Heater connector on Fixture

2.3.3.4.2. Power On System Test

Set Board Data via nvram

16:57:04:326| U-Boot SPL 1.0.1 (Jun 17 2016 - 15:47:14)

16:57:04:326| HW WDT Disabled

16:57:04:326| DEV ID = 0xcf1e

16:57:04:326| PCIE CFG DEV ID = 0x8022

16:57:04:326| OTP offset(0x8): 0x78f01c01

16:57:04:326| OTP offset(0x9): 0xfe280818

16:57:04:326| OTP offset(0xa): 0xc01b0

16:57:04:326| OTP offset(0xb): 0x0

16:57:04:326| OTP offset(0xc): 0x4400000

16:57:04:326| OTP offset(0xd): 0xbccde230

16:57:04:326| OTP offset(0xe): 0x1035b17f

16:57:04:326| OTP offset(0xf): 0x2c1c

16:57:04:326| SKU ID = 0x0

16:57:04:326| DDR type: DDR3

16:57:04:326| MEMC 0 DDR speed = 800MHz

16:57:04:326| ddr_init2: Calling soc_ddr40_set_shmoo_dram_config

16:57:04:326| ddr_init2: Calling soc_ddr40_phy_calibrate

16:57:04:326| C01. Check Power Up Reset_Bar

16:57:04:326| C02. Config and Release PLL from reset

16:57:04:326| C03. Poll PLL Lock

16:57:04:326| C04. Calibrate ZQ (ddr40_phy_calib_zq)

16:57:04:326| C05. DDR PHY VTT On (Virtual VTT setup) DISABLE all Virtual VTT

16:57:04:326| C06. DDR40_PHY_DDR3_MISC

16:57:04:326| C07. VDL Calibration

16:57:04:326| C07.1

16:57:04:326| C07.2

16:57:04:326| C07.4

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16:57:04:326| C07.4.1

16:57:04:326| C07.4.4

16:57:04:326| VDL calibration result: 0x30000003 (cal_steps = 0)

16:57:04:326| C07.4.5

16:57:04:326| C07.4.6

16:57:04:326| C07.5

16:57:04:326| C08. DDR40_PHY_DDR3_MISC : Start DDR40_PHY_RDLY_ODT....

16:57:04:326| C09. Start ddr40_phy_autoidle_on (MEM_SYS_PARAM_PHY_AUTO_IDLE) ....

16:57:04:326| C10. Wait for Phy Ready

16:57:04:326| Programming controller register

16:57:04:838| ddr_init2: Calling soc_ddr40_shmoo_ctl

16:57:04:861| Validate Shmoo parameters stored in flash ..... OK

16:57:04:861| Press Ctrl-C to run Shmoo ..... skipped

16:57:05:358| Restoring Shmoo parameters from flash ..... done

16:57:05:371| Running simple memory test ..... OK

16:57:05:589| DDR Tune Completed

16:57:05:589| (ONFI), MT29F4G08ABADAWP ,

16:57:05:589| *ERROR* Invalid strap options for this NAND: page=1 type=2

16:57:05:589| Overriding invalid strap options: strap_type=2

16:57:05:589| 128 KiB blocks, 2 KiB pages, 16B OOB, 8-bit

16:57:05:589| NAND: chipsize 512 MiB

16:57:05:589|

16:57:05:788|

16:57:05:788| U-Boot 1.0.1 (Jun 17 2016 - 15:47:14)

16:57:05:788|

16:57:05:788| DRAM: 16 MiB

16:57:05:788| WARNING: Caches not enabled

16:57:05:788| NAND: (ONFI), MT29F4G08ABADAWP ,

16:57:05:788| *ERROR* Invalid strap options for this NAND: page=1 type=2

16:57:05:788| Overriding invalid strap options: strap_type=2

16:57:05:788| 128 KiB blocks, 2 KiB pages, 16B OOB, 8-bit

16:57:05:788| NAND: chipsize 512 MiB

16:57:05:788| MMC: iproc_mmc: 0

16:57:05:788|

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16:57:05:895| soc_pcie_hw_init : port->reg_base = 0x18012000 , its value = 0x1

16:57:06:229| PCIe port in RC mode

16:57:06:229|

16:57:06:229| ==>PCIE: LINKSTA reg 0xbe val 0x9012

16:57:06:229|

16:57:06:229| **********

16:57:06:229| port 0 is active!!

16:57:06:229| **********

16:57:06:229| membase 0x8000000 memlimit 0x10000000

16:57:06:229| 00:00.00 - 14e4:43a2 - Network controller

16:57:06:229|

16:57:06:229| pci_iproc_init_board : hose->last_busno = 0x0

16:57:06:229|

16:57:06:229| Extended Config

16:57:06:229| i=0 <0> 0x14e4 0x8022 0x0146 0x0010 0x0011 0x0200 0x0000 0x0001

16:57:06:229| i=8 <8> 0x0000 0x0000 0x0000 0x0000 0x0100 0x0004 0x0000 0x0000

16:57:06:229| i=16 <10> 0x0800 0x1000 0x0001 0x0001 0x0000 0x0000 0x0000 0x0000

16:57:06:229| i=24 <18> 0x0000 0x0000 0x0048 0x0000 0x0000 0x0000 0x01a9 0x0000

16:57:06:229| i=32 <20> 0x0000 0x0000 0x0000 0x0000 0xac01 0xc803 0x2008 0x0000

16:57:06:229| i=40 <28> 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000

16:57:06:229| i=48 <30> 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000

16:57:06:229| i=56 <38> 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000

16:57:06:229| i=64 <40> 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000

16:57:06:229| i=72 <48> 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000

16:57:06:229| i=80 <50> 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0010 0x0042

16:57:06:229| i=88 <58> 0x8000 0x0000 0x2c10 0x0010 0x5c12 0x0065 0x0000 0x9012

16:57:06:229| i=96 <60> 0x0000 0x0000 0x0000 0x0040 0x0000 0x0001 0x0000 0x0000

16:57:06:229| i=104 <68> 0x001f 0x0008 0x0000 0x0000 0x0000 0x0000 0x0002 0x0000

16:57:06:229| i=112 <70> 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000

16:57:06:229| i=120 <78> 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000

16:57:06:229|

16:57:06:229|

16:57:06:229| Standard Config

16:57:06:229| i=0 <0> 0x43a214e4 0x00100146 0x02800003 0x00000000

16:57:06:229| i=4 <4> 0x08000004 0x00000000 0x08200004 0x00000000

16:57:06:229| i=8 <8> 0x00000000 0x00000000 0x00000000 0x063314e4

16:57:06:229| i=12 <c> 0x00000000 0x00000048 0x00000000 0x000001a9

16:57:06:229| i=16 <10> 0x00000000 0x00000000 0xfe035801 0x00004108

16:57:06:229| i=20 <14> 0x00000000 0x00000000 0x00806805 0x00000000

16:57:06:229| i=24 <18> 0x00000000 0x00000000 0x0044ac09 0x00000101

16:57:06:229| i=28 <1c> 0x18103000 0x00000000 0x00000000 0x00000000

16:57:06:229| i=32 <20> 0x18003000 0x00000000 0x00000000 0x00000000

16:57:06:229| i=36 <24> 0x00000000 0x00000200 0x00000000 0x00000000

16:57:06:229| i=40 <28> 0x00000000 0x00000000 0x00010040 0x00020010

16:57:06:229| i=44 <2c> 0x00008f81 0x00102c10 0x0046dc12 0x10120000

16:57:06:229| i=48 <30> 0x00000000 0x00000000 0x00000000 0x00000000

16:57:06:229| i=52 <34> 0x0008081f 0x00000000 0x00000000 0x00000001

16:57:06:229| i=56 <38> 0x00000000 0x00000000 0x00000000 0x00000000

16:57:06:229| i=60 <3c> 0x00000000 0x00000000 0x00000000 0x00000000

16:57:06:229|

16:57:06:229| BAR-0: 0x08000004

16:57:06:229|

16:57:06:229| BAR-1: 0x00000000

16:57:06:229|

16:57:06:229| BAR-2: 0x08200004

16:57:06:229|

16:57:06:229| BAR-3: 0x00000000

16:57:06:229|

16:57:06:229| BAR-4: 0x00000000

16:57:06:229|

16:57:06:229| BAR-5: 0x00000000

16:57:06:229|

16:57:06:229| PCI: Bus Dev VenId DevId Class Int

16:57:06:229| Done PCI initialization

16:57:06:229| In: serial

16:57:06:229| Out: serial

16:57:06:229| Err: serial

16:57:06:229| arm_clk=1000MHz, axi_clk=500MHz, apb_clk=250MHz, arm_periph_clk=500MHz

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16:57:06:229| Enabling icache and dcache

16:57:06:229| Enabling l2cache

16:57:06:229| Net: Registering eth

16:57:06:229| Broadcom BCM IPROC Ethernet driver 0.1

16:57:06:229| Using GMAC1 (0x18023000)

16:57:06:229| et0: ethHw_chipAttach: Chip ID: 0xcf1e; phyaddr: 0x1e

16:57:06:229| bcm_robo_attach: devid: 0xb

16:57:06:229| bcmiproc_eth-0

16:57:06:229|

16:57:06:229| **

16:57:06:229| Please add diag support DIAGS_BOARD to:

16:57:06:229| BCM953022K

16:57:06:229| BCM953025K

16:57:06:229| BCM953025K_REV2

16:57:06:229| BCM958622HR

16:57:06:229| BCM958623HR

16:57:06:229| BCM958522ER

16:57:06:229| BCM958625HR

16:57:06:229| in env before you can run any diags

16:57:06:229| **

16:57:06:229| COM2 is not configured due to board type

16:57:06:229| COM3 is not configured due to board type

16:57:06:229| NVRAM_MAGIC found at offset 100000

16:57:06:229| nvram_init: ret 0

16:57:06:229| Hit any key to stop autoboot: 0

16:57:06:260| u-boot>

16:57:06:260| u-boot>

##

<time:16:57:06> DDR type: DDR3

##

##

<time:16:57:06> MEMC 0 DDR speed = 800MHz

##

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```
##################################################
# <time:16:57:06> DRAM:    16 MiB                 #
##################################################

#########################################################################################################
# <time:16:57:06> arm_clk=1000MHz, axi_clk=500MHz, apb_clk=250MHz, arm_periph_clk=500MHz                #
#########################################################################################################
```

2.3.3.4.3. Check Ethernet Port

Check LAN port and WAN port

```
setenv ipaddr 192.168.1.11
16:57:06:306| u-boot> ping 192.168.1.120
16:57:06:321| Using bcmiproc_eth-0 device
16:57:06:321| host 192.168.1.120 is alive
16:57:11:333| u-boot>
######################################################
# <time:16:57:11> check WAN port ok                  #
######################################################
setenv ipaddr 192.168.100.11
16:57:11:374| u-boot> ping 192.168.100.120
16:57:11:394| Using bcmiproc_eth-0 device
16:57:11:394|
16:57:31:399| Abort
16:57:31:399| ping failed; host 192.168.100.120 is not alive
16:57:31:409| u-boot> <INTERRUPT>
16:57:31:409| u-boot> ping 192.168.100.120
16:57:40:115| Using bcmiproc_eth-0 device
16:57:40:115| host 192.168.100.120 is alive
16:57:45:116| u-boot>
######################################################
# <time:16:57:45> check LAN port ok                  #
######################################################
```

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2.3.3.4.4. Check Uboot Version

Check Uboot version

version

16:57:45:133|

16:57:45:133| U-Boot 1.0.1 (Jun 17 2016 - 15:47:14)

16:57:45:158| arm-uclibc-linux-2.6.36-gcc (Buildroot 2012.02) 4.5.3

16:57:45:158| GNU ld (GNU Binutils) 2.21.1

16:57:45:158| u-boot>

##
######

<time:16:57:45> Check default Uboot Version U-Boot 1.0.1 (Jun 17 2016 - 15:47:14) ok

##
######

2.3.3.4.5. Set Board Data

Set Board Information

nvram set et0macaddr=CC:37:AB:BB:DC:C0

16:57:45:826| u-boot> nvram set ethaddr=CC:37:AB:BB:DC:C0

16:57:46:431| u-boot> nvram set serial#=EC1609000280

16:57:46:943| u-boot> nvram set band_plan=FCC

16:57:47:480| u-boot> nvram set country_code=US

16:57:48:334| u-boot> nvram commit

16:57:48:832| nvram_commit: Writing flash partitions

16:57:48:832| u-boot>

2.3.3.4.6. Check Board Information

Check Board Information that written before

nvram get serial#

16:57:52:402| EC1609000280

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16:57:52:402| u-boot>

##

<time:16:57:53> check serial# == EC1609000280 ok

##

nvram get et0macaddr

16:57:53:798| CC:37:AB:BB:DC:C0

16:57:53:798| u-boot>

###

<time:16:57:54> check et0macaddr == CC:37:AB:BB:DC:C0 ok

###

nvram get ethaddr

16:57:55:106| CC:37:AB:BB:DC:C0

16:57:55:106| u-boot>

###

<time:16:57:56> check ethaddr == CC:37:AB:BB:DC:C0 ok

###

nvram get band_plan

16:57:56:464| FCC

16:57:56:464| u-boot>

##

<time:16:57:57> check band_plan == FCC ok

##

nvram get country_code

16:57:57:836| US

16:57:57:836| u-boot>

##

<time:16:57:57> check country_code == US ok

##

2.3.3.4.7. DDR Test

Check DDR Read/Write

mtest 0x82000000 0x8f000000 0x05050505 1

16:57:58:486|

16:57:58:486| Pattern 05050505 Writing... Reading...Tested 1 iteration(s) with 0 errors.

16:58:06:472| u-boot>

2.3.3.4.8. Check Led

Check Bi-Color Led, Red and Green

mw 0x18000068 0x000003

16:58:06:905| u-boot> mw 0x18000064 0x000002

16:58:06:916| u-boot>

****

* Check led Red *

****

* PASS *

****

mw 0x18000064 0x000001

16:58:10:077| u-boot>

****

* Check led Green *

****

* PASS *

****

mw 0x18000064 0x000003

16:58:11:551| u-boot>

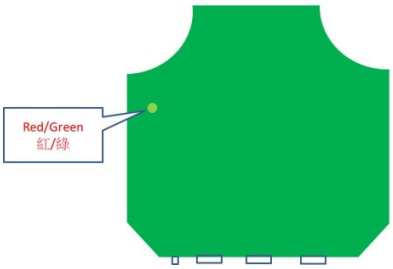
****

* Check Led in kernel *

****

* PASS *

****



2.3.3.4.9. Load Kernel Code

Load Kernel Code to Bank 1 and Bank 2

setenv serverip 192.168.1.120

16:58:12:057| u-boot> setenv ipaddr 192.168.1.101

16:58:12:504| u-boot> ping 192.168.1.120

16:58:17:837| Using bcmiproc_eth-0 device

16:58:17:837| host 192.168.1.120 is alive

16:58:17:837| u-boot>

##

<time:16:58:17> Ethernet Connected ok

##

run update_trx

16:58:18:088| Using bcmiproc_eth-0 device

16:58:18:088| TFTP from server 192.168.1.120; our IP address is 192.168.1.101

16:58:18:088| Filename 'rootfs.bin'.

16:58:18:088| Load address: 0x82000000

16:58:18:088| Loading: ##

16:58:18:345| ##

16:58:18:578| ##

16:58:18:777| ##

16:58:19:010| ##

16:58:19:243| ##

16:58:19:489| ##

16:58:19:695| ##

16:58:19:916| ##

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16:58:20:138| ##
16:58:20:426| ##
16:58:20:666| ##
16:58:20:927| ##
16:58:21:145| ##
16:58:21:375| ##
16:58:21:613| ##
16:58:21:935| ##
16:58:22:184| ##
16:58:22:413| ##
16:58:22:638| ##
16:58:22:844| ##
16:58:23:069| ##
16:58:23:288| ##
16:58:23:511| ##
16:58:23:751| ##
16:58:23:963| ##
16:58:24:190| ##
16:58:24:431| ##
16:58:24:646| ##
16:58:24:876| ##
16:58:25:097| ##
16:58:25:326| ##
16:58:25:546| ##
16:58:25:752| ##
16:58:25:973| ##
16:58:26:205| ##
16:58:26:429| ##
16:58:26:648| ##
16:58:26:871| ##
16:58:27:095| ##
16:58:27:315| ##
16:58:27:544| ##
16:58:27:768| ##

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16:58:27:990| ##
16:58:28:228| ##
16:58:28:435| ##
16:58:28:665| ##
16:58:28:886| ##
16:58:29:146| ##
16:58:29:370| ##
16:58:29:587| ##
16:58:29:809| ##
16:58:30:039| ##
16:58:30:257| ##
16:58:30:475| ##
16:58:30:694| ##
16:58:30:912| ##
16:58:31:129| ##
16:58:31:348| ##
16:58:31:567| ##
16:58:31:813| ##
16:58:32:021| ##
16:58:32:242| ##
16:58:32:517| ##
16:58:32:736| ##
16:58:32:960| ##
16:58:33:187| ##
16:58:33:412| ##
16:58:33:637| ##
16:58:33:853| ##
16:58:34:081| ##
16:58:34:302| ##
16:58:34:531| ##
16:58:34:749| ##
16:58:34:988| ##
16:58:35:217| ##
16:58:35:436| ##

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16:58:35:664| ##

16:58:35:889| done

16:58:35:889| Bytes transferred = 25952256 (18c0000 hex)

16:58:35:889|

16:58:36:467| NAND erase: device 0 offset 0x400000, size 0x2800000

16:58:36:467|

16:58:36:467| Erasing at 0x400000 -- 0% complete.

16:58:36:467| Erasing at 0x460000 -- 1% complete.

16:58:36:467| Erasing at 0x4c0000 -- 2% complete.

16:58:36:467| Erasing at 0x520000 -- 3% complete.

16:58:36:467| Erasing at 0x580000 -- 4% complete.

16:58:36:467| Erasing at 0x5e0000 -- 5% complete.

16:58:36:467| Erasing at 0x660000 -- 6% complete.

16:58:36:467| Erasing at 0x6c0000 -- 7% complete.

16:58:36:467| Erasing at 0x720000 -- 8% complete.

16:58:36:467| Erasing at 0x780000 -- 9% complete.

16:58:36:467| Erasing at 0x7e0000 -- 10% complete.

16:58:36:467| Erasing at 0x860000 -- 11% complete.

16:58:36:467| Erasing at 0x8c0000 -- 12% complete.

16:58:36:467| Erasing at 0x920000 -- 13% complete.

16:58:36:467| Erasing at 0x980000 -- 14% complete.

16:58:36:467| Erasing at 0x9e0000 -- 15% complete.

16:58:36:467| Erasing at 0xa60000 -- 16% complete.

16:58:36:467| Erasing at 0xac0000 -- 17% complete.

16:58:36:467| Erasing at 0xb20000 -- 18% complete.

16:58:36:467| Erasing at 0xb80000 -- 19% complete.

16:58:36:467| Erasing at 0xbe0000 -- 20% complete.

16:58:36:467| Erasing at 0xc60000 -- 21% complete.

16:58:36:467| Erasing at 0xcc0000 -- 22% complete.

16:58:36:467| Erasing at 0xd20000 -- 23% complete.

16:58:36:467| Erasing at 0xd80000 -- 24% complete.

16:58:36:467| Erasing at 0xde0000 -- 25% complete.

16:58:36:467| Erasing at 0xe60000 -- 26% complete.

16:58:36:467| Erasing at 0xec0000 -- 27% complete.

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16:58:36:467| Erasing at 0xf20000 -- 28% complete.
16:58:36:467| Erasing at 0xf80000 -- 29% complete.
16:58:36:467| Erasing at 0xfe0000 -- 30% complete.
16:58:36:467| Erasing at 0x1060000 -- 31% complete.
16:58:36:467| Erasing at 0x10c0000 -- 32% complete.
16:58:36:467| Erasing at 0x1120000 -- 33% complete.
16:58:36:467| Erasing at 0x1180000 -- 34% complete.
16:58:36:467| Erasing at 0x11e0000 -- 35% complete.
16:58:36:467| Erasing at 0x1260000 -- 36% complete.
16:58:36:467| Erasing at 0x12c0000 -- 37% complete.
16:58:36:467| Erasing at 0x1320000 -- 38% complete.
16:58:36:467| Erasing at 0x1380000 -- 39% complete.
16:58:36:467| Erasing at 0x13e0000 -- 40% complete.
16:58:36:467| Erasing at 0x1460000 -- 41% complete.
16:58:36:467| Erasing at 0x14c0000 -- 42% complete.
16:58:36:467| Erasing at 0x1520000 -- 43% complete.
16:58:36:467| Erasing at 0x1580000 -- 44% complete.
16:58:36:467| Erasing at 0x15e0000 -- 45% complete.
16:58:36:467| Erasing at 0x1660000 -- 46% complete.
16:58:36:467| Erasing at 0x16c0000 -- 47% complete.
16:58:36:467| Erasing at 0x1720000 -- 48% complete.
16:58:36:467| Erasing at 0x1780000 -- 49% complete.
16:58:36:467| Erasing at 0x17e0000 -- 50% complete.
16:58:36:467| Erasing at 0x1860000 -- 51% complete.
16:58:36:467| Erasing at 0x18c0000 -- 52% complete.
16:58:36:467| Erasing at 0x1920000 -- 53% complete.
16:58:36:467| Erasing at 0x1980000 -- 54% complete.
16:58:36:467| Erasing at 0x19e0000 -- 55% complete.
16:58:36:467| Erasing at 0x1a60000 -- 56% complete.
16:58:36:467| Erasing at 0x1ac0000 -- 57% complete.
16:58:36:467| Erasing at 0x1b20000 -- 58% complete.
16:58:36:467| Erasing at 0x1b80000 -- 59% complete.
16:58:36:467| Erasing at 0x1be0000 -- 60% complete.
16:58:36:467| Erasing at 0x1c60000 -- 61% complete.

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16:58:36:467| Erasing at 0x1cc0000 -- 62% complete.
16:58:36:467| Erasing at 0x1d20000 -- 63% complete.
16:58:36:467| Erasing at 0x1d80000 -- 64% complete.
16:58:36:467| Erasing at 0x1de0000 -- 65% complete.
16:58:36:467| Erasing at 0x1e60000 -- 66% complete.
16:58:36:467| Erasing at 0x1ec0000 -- 67% complete.
16:58:36:467| Erasing at 0x1f20000 -- 68% complete.
16:58:36:467| Erasing at 0x1f80000 -- 69% complete.
16:58:36:467| Erasing at 0x1fe0000 -- 70% complete.
16:58:36:467| Erasing at 0x2060000 -- 71% complete.
16:58:36:467| Erasing at 0x20c0000 -- 72% complete.
16:58:36:467| Erasing at 0x2120000 -- 73% complete.
16:58:36:467| Erasing at 0x2180000 -- 74% complete.
16:58:36:467| Erasing at 0x21e0000 -- 75% complete.
16:58:36:467| Erasing at 0x2260000 -- 76% complete.
16:58:36:467| Erasing at 0x22c0000 -- 77% complete.
16:58:36:467| Erasing at 0x2320000 -- 78% complete.
16:58:36:467| Erasing at 0x2380000 -- 79% complete.
16:58:36:467| Erasing at 0x23e0000 -- 80% complete.
16:58:36:467| Erasing at 0x2460000 -- 81% complete.
16:58:36:467| Erasing at 0x24c0000 -- 82% complete.
16:58:36:467| Erasing at 0x2520000 -- 83% complete.
16:58:36:467| Erasing at 0x2580000 -- 84% complete.
16:58:36:467| Erasing at 0x25e0000 -- 85% complete.
16:58:36:467| Erasing at 0x2660000 -- 86% complete.
16:58:36:467| Erasing at 0x26c0000 -- 87% complete.
16:58:36:467| Erasing at 0x2720000 -- 88% complete.
16:58:36:467| Erasing at 0x2780000 -- 89% complete.
16:58:36:467| Erasing at 0x27e0000 -- 90% complete.
16:58:36:467| Erasing at 0x2860000 -- 91% complete.
16:58:36:467| Erasing at 0x28c0000 -- 92% complete.
16:58:36:467| Erasing at 0x2920000 -- 93% complete.
16:58:36:467| Erasing at 0x2980000 -- 94% complete.
16:58:36:467| Erasing at 0x29e0000 -- 95% complete.

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16:58:36:467| Erasing at 0x2a60000 -- 96% complete.

16:58:36:467| Erasing at 0x2ac0000 -- 97% complete.

16:58:36:467| Erasing at 0x2b20000 -- 98% complete.

16:58:36:467| Erasing at 0x2b80000 -- 99% complete.

16:58:36:467| Erasing at 0x2be0000 -- 100% complete.

16:58:36:467| OK

16:58:36:480|

16:58:36:480| NAND write: device 0 offset 0x400000, size 0x18c0000

16:58:36:480| 25952256 bytes written: OK

16:58:43:849| u-boot>

##

<time:16:58:43> run update_trx ok

##

##

<time:16:58:43> run update_trx ok

##

run update_trx2

16:58:44:140| Using bcmiproc_eth-0 device

16:58:44:140| TFTP from server 192.168.1.120; our IP address is 192.168.1.101

16:58:44:140| Filename 'rootfs.bin'.

16:58:44:140| Load address: 0x82000000

16:58:44:140| Loading: ###

16:58:44:362| ###

16:58:44:567| ###

16:58:44:787| ###

16:58:45:013| ###

16:58:45:225| ###

16:58:45:450| ###

16:58:45:673| ###

16:58:45:896| ###

16:58:46:105| ###

16:58:46:323| ###

16:58:46:548| ###

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16:58:46:776| ##
16:58:46:995| ##
16:58:47:219| ##
16:58:47:449| ##
16:58:47:667| ##
16:58:47:891| ##
16:58:48:102| ##
16:58:48:341| ##
16:58:48:572| ##
16:58:48:791| ##
16:58:49:018| ##
16:58:49:231| ##
16:58:49:461| ##
16:58:49:682| ##
16:58:49:914| ##
16:58:50:130| ##
16:58:50:354| ##
16:58:50:576| ##
16:58:50:817| ##
16:58:51:045| ##
16:58:51:285| ##
16:58:51:490| ##
16:58:51:714| ##
16:58:51:960| ##
16:58:52:159| ##
16:58:52:383| ##
16:58:52:611| ##
16:58:52:814| ##
16:58:53:038| ##
16:58:53:270| ##
16:58:53:474| ##
16:58:53:696| ##
16:58:53:916| ##
16:58:54:145| ##

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16:58:54:373| ##
16:58:54:592| ##
16:58:54:817| ##
16:58:55:039| ##
16:58:55:248| ##
16:58:55:468| ##
16:58:55:694| ##
16:58:55:904| ##
16:58:56:130| ##
16:58:56:348| ##
16:58:56:590| ##
16:58:56:818| ##
16:58:57:036|
16:58:57:058| ##
16:58:57:261| ##
16:58:57:484| ##
16:58:57:712| ##
16:58:57:915| ##
16:58:58:159| ##
16:58:58:395| ##
16:58:58:637| ##
16:58:58:843| ##
16:58:59:069| ##
16:58:59:288| ##
16:58:59:511| ##
16:58:59:740| ##
16:58:59:947| ##
16:59:00:176| ##
16:59:00:394| ##
16:59:00:615| ##
16:59:00:842| ##
16:59:01:070| ##
16:59:01:285| ##
16:59:01:500| done

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16:59:01:500| Bytes transferred = 25952256 (18c0000 hex)

16:59:01:500|

16:59:01:518| NAND erase: device 0 offset 0x2c00000, size 0x2800000

16:59:01:518|

16:59:02:095| Erasing at 0x2c00000 -- 0% complete.

16:59:02:095| Erasing at 0x2c60000 -- 1% complete.

16:59:02:095| Erasing at 0x2cc0000 -- 2% complete.

16:59:02:095| Erasing at 0x2d20000 -- 3% complete.

16:59:02:095| Erasing at 0x2d80000 -- 4% complete.

16:59:02:095| Erasing at 0x2de0000 -- 5% complete.

16:59:02:095| Erasing at 0x2e60000 -- 6% complete.

16:59:02:095| Erasing at 0x2ec0000 -- 7% complete.

16:59:02:095| Erasing at 0x2f20000 -- 8% complete.

16:59:02:095| Erasing at 0x2f80000 -- 9% complete.

16:59:02:095| Erasing at 0x2fe0000 -- 10% complete.

16:59:02:095| Erasing at 0x3060000 -- 11% complete.

16:59:02:095| Erasing at 0x30c0000 -- 12% complete.

16:59:02:095| Erasing at 0x3120000 -- 13% complete.

16:59:02:095| Erasing at 0x3180000 -- 14% complete.

16:59:02:095| Erasing at 0x31e0000 -- 15% complete.

16:59:02:095| Erasing at 0x3260000 -- 16% complete.

16:59:02:095| Erasing at 0x32c0000 -- 17% complete.

16:59:02:095| Erasing at 0x3320000 -- 18% complete.

16:59:02:095| Erasing at 0x3380000 -- 19% complete.

16:59:02:095| Erasing at 0x33e0000 -- 20% complete.

16:59:02:095| Erasing at 0x3460000 -- 21% complete.

16:59:02:095| Erasing at 0x34c0000 -- 22% complete.

16:59:02:095| Erasing at 0x3520000 -- 23% complete.

16:59:02:095| Erasing at 0x3580000 -- 24% complete.

16:59:02:095| Erasing at 0x35e0000 -- 25% complete.

16:59:02:095| Erasing at 0x3660000 -- 26% complete.

16:59:02:095| Erasing at 0x36c0000 -- 27% complete.

16:59:02:095| Erasing at 0x3720000 -- 28% complete.

16:59:02:095| Erasing at 0x3780000 -- 29% complete.

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16:59:02:095| Erasing at 0x37e0000 -- 30% complete.
16:59:02:095| Erasing at 0x3860000 -- 31% complete.
16:59:02:095| Erasing at 0x38c0000 -- 32% complete.
16:59:02:095| Erasing at 0x3920000 -- 33% complete.
16:59:02:095| Erasing at 0x3980000 -- 34% complete.
16:59:02:095| Erasing at 0x39e0000 -- 35% complete.
16:59:02:095| Erasing at 0x3a60000 -- 36% complete.
16:59:02:095| Erasing at 0x3ac0000 -- 37% complete.
16:59:02:095| Erasing at 0x3b20000 -- 38% complete.
16:59:02:095| Erasing at 0x3b80000 -- 39% complete.
16:59:02:095| Erasing at 0x3be0000 -- 40% complete.
16:59:02:095| Erasing at 0x3c60000 -- 41% complete.
16:59:02:095| Erasing at 0x3cc0000 -- 42% complete.
16:59:02:095| Erasing at 0x3d20000 -- 43% complete.
16:59:02:095| Erasing at 0x3d80000 -- 44% complete.
16:59:02:095| Erasing at 0x3de0000 -- 45% complete.
16:59:02:095| Erasing at 0x3e60000 -- 46% complete.
16:59:02:095| Erasing at 0x3ec0000 -- 47% complete.
16:59:02:095| Erasing at 0x3f20000 -- 48% complete.
16:59:02:095| Erasing at 0x3f80000 -- 49% complete.
16:59:02:095| Erasing at 0x3fe0000 -- 50% complete.
16:59:02:095| Erasing at 0x4060000 -- 51% complete.
16:59:02:095| Erasing at 0x40c0000 -- 52% complete.
16:59:02:095| Erasing at 0x4120000 -- 53% complete.
16:59:02:095| Erasing at 0x4180000 -- 54% complete.
16:59:02:095| Erasing at 0x41e0000 -- 55% complete.
16:59:02:095| Erasing at 0x4260000 -- 56% complete.
16:59:02:095| Erasing at 0x42c0000 -- 57% complete.
16:59:02:095| Erasing at 0x4320000 -- 58% complete.
16:59:02:095| Erasing at 0x4380000 -- 59% complete.
16:59:02:095| Erasing at 0x43e0000 -- 60% complete.
16:59:02:095| Erasing at 0x4460000 -- 61% complete.
16:59:02:095| Erasing at 0x44c0000 -- 62% complete.
16:59:02:095| Erasing at 0x4520000 -- 63% complete.

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16:59:02:095| Erasing at 0x4580000 -- 64% complete.
16:59:02:095| Erasing at 0x45e0000 -- 65% complete.
16:59:02:095| Erasing at 0x4660000 -- 66% complete.
16:59:02:095| Erasing at 0x46c0000 -- 67% complete.
16:59:02:095| Erasing at 0x4720000 -- 68% complete.
16:59:02:095| Erasing at 0x4780000 -- 69% complete.
16:59:02:095| Erasing at 0x47e0000 -- 70% complete.
16:59:02:095| Erasing at 0x4860000 -- 71% complete.
16:59:02:095| Erasing at 0x48c0000 -- 72% complete.
16:59:02:095| Erasing at 0x4920000 -- 73% complete.
16:59:02:095| Erasing at 0x4980000 -- 74% complete.
16:59:02:095| Erasing at 0x49e0000 -- 75% complete.
16:59:02:095| Erasing at 0x4a60000 -- 76% complete.
16:59:02:095| Erasing at 0x4ac0000 -- 77% complete.
16:59:02:095| Erasing at 0x4b20000 -- 78% complete.
16:59:02:095| Erasing at 0x4b80000 -- 79% complete.
16:59:02:095| Erasing at 0x4be0000 -- 80% complete.
16:59:02:095| Erasing at 0x4c60000 -- 81% complete.
16:59:02:095| Erasing at 0x4cc0000 -- 82% complete.
16:59:02:095| Erasing at 0x4d20000 -- 83% complete.
16:59:02:095| Erasing at 0x4d80000 -- 84% complete.
16:59:02:095| Erasing at 0x4de0000 -- 85% complete.
16:59:02:095| Erasing at 0x4e60000 -- 86% complete.
16:59:02:095| Erasing at 0x4ec0000 -- 87% complete.
16:59:02:095| Erasing at 0x4f20000 -- 88% complete.
16:59:02:095| Erasing at 0x4f80000 -- 89% complete.
16:59:02:095| Erasing at 0x4fe0000 -- 90% complete.
16:59:02:095| Erasing at 0x5060000 -- 91% complete.
16:59:02:095| Erasing at 0x50c0000 -- 92% complete.
16:59:02:095| Erasing at 0x5120000 -- 93% complete.
16:59:02:095| Erasing at 0x5180000 -- 94% complete.
16:59:02:095| Erasing at 0x51e0000 -- 95% complete.
16:59:02:095| Erasing at 0x5260000 -- 96% complete.
16:59:02:095| Erasing at 0x52c0000 -- 97% complete.

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16:59:02:095| Erasing at 0x5320000 -- 98% complete.

16:59:02:095| Erasing at 0x5380000 -- 99% complete.

16:59:02:095| Erasing at 0x53e0000 -- 100% complete.

16:59:02:095| OK

16:59:02:095|

16:59:02:095| NAND write: device 0 offset 0x2c00000, size 0x18c0000

16:59:02:106| 25952256 bytes written: OK

16:59:09:476| u-boot>

2.3.3.4.10. Set Kernel Code Information

Set Kernel information

nvram set bootver=V1.3.4.0-ECWO7220-L-0000

16:59:10:170| u-boot> nvram set altbootver=V1.3.4.0-ECWO7220-L-0000

16:59:10:898| u-boot> nvram commit

16:59:11:106| nvram_commit: Writing flash partitions

16:59:11:106| u-boot>

2.3.3.4.11. Check Kernel Code Information

Check kernel code information

get system version

16:59:59:873| V1.3.4.0-ECWO7220-L-0000

16:59:59:953| ECWO7220-L-bbdcc0#

```
################################################################################
# <time:16:59:59> check system version == V1.3.4.0-ECWO7220-L-0000 ok          #
################################################################################
```

get system altversion

17:00:00:333| V1.3.4.0-ECWO7220-L-0000

17:00:00:353| ECWO7220-L-bbdcc0#

```
################################################################################
# <time:17:00:00> check system altversion == V1.3.4.0-ECWO7220-L-0000 ok       #
################################################################################
```

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2.3.3.4.12. Check PCIe

Check PCIE interface of 2.4g and 5g RF card

ifconfig wlan0

17:00:00:462| wlan0 Link encap:Ethernet HWaddr CC:37:AB:BB:DC:C0

17:00:00:462| UP BROADCAST RUNNING MULTICAST MTU:1500 Metric:1

17:00:00:462| RX packets:0 errors:0 dropped:0 overruns:0 frame:0

17:00:00:462| TX packets:0 errors:0 dropped:0 overruns:0 carrier:0

17:00:00:462| collisions:0 txqueuelen:1000

17:00:00:462| RX bytes:0 (0.0 B) TX bytes:0 (0.0 B)

17:00:00:462| Interrupt:163

17:00:00:462|

17:00:00:462| ECWO7220-L-bbdcc0# ifconfig wlan1

17:00:00:541| wlan1 Link encap:Ethernet HWaddr CC:37:AB:BB:DC:D0

17:00:00:541| UP BROADCAST RUNNING MULTICAST MTU:1500 Metric:1

17:00:00:541| RX packets:0 errors:0 dropped:0 overruns:0 frame:0

17:00:00:541| TX packets:0 errors:0 dropped:0 overruns:0 carrier:0

17:00:00:541| collisions:0 txqueuelen:1000

17:00:00:541| RX bytes:0 (0.0 B) TX bytes:0 (0.0 B)

17:00:00:541| Interrupt:169

17:00:00:541|

17:00:00:541| ECWO7220-L-bbdcc0#

2.3.3.4.13. PWC Test

PWC Test. This test required Arduino Board

2.3.3.5 Test Log



EC1609000280_PASS_N_1_165648.cap

2.3.3.6 Test Estimated Cycle Time

Station Name	Max of Units	Estimation (Second (s) of Time)
P/T	1	220 (s)

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2.3.4 System Level Final Test (FT2)

2.3.4.1 Test Environment Setup

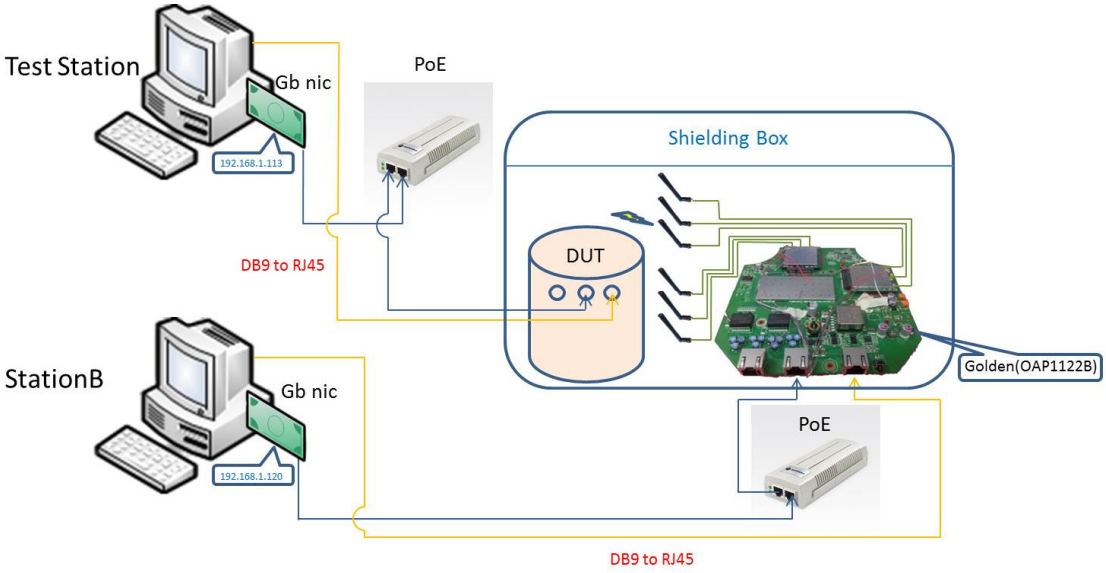


Figure 5 FT2 Testing Configuration

2.3.4.2 Hardware Requirement

1	Pentium PC (or above) running WinXP/Win 7	x 2
2	Golden Unit(OAP1122B-0216-EC)	x 1
3	PoE	x 2
4	Gb nic	x 2
5	RS232 to RJ45 console	x 2
6	RF Cable with antenna	x 6
7	Shielding Box	x 1
8	RJ45 cable	x 4

Table 7 FT2 Testing HW Requirement

2.3.4.3 Software Requirement

1	Microsoft OS Windows XP or above	
2	ActiveTCL: Tcl v8.5.15	

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Table 8 FT2 Testing Software Requirement

2.3.4.4 Test Requirements

Run requested Test Items without any fail condition.

2.3.4.4.1. Get and Check Board Data

Check Board Data that write before

```
nvram get serial#
13:19:48:832| EC1609000280
13:19:48:832| u-boot>
#####################################################
# <time:13:19:48> check serial# == EC1609000280 ok      #
#####################################################
nvram get et0macaddr
13:19:49:168| CC:37:AB:BB:DC:C0
13:19:49:168| u-boot>
#############################################################################
# <time:13:19:49> check et0macaddr == CC:37:AB:BB:DC:C0 ok                   #
#############################################################################
nvram get ethaddr
13:19:49:448| CC:37:AB:BB:DC:C0
13:19:49:448| u-boot>
#############################################################################
# <time:13:19:49> check ethaddr == CC:37:AB:BB:DC:C0 ok                      #
#############################################################################
nvram get band_plan
13:19:49:770| FCC
13:19:49:770| u-boot>
#####################################################
# <time:13:19:49> check band_plan == FCC ok                #
#####################################################
nvram get country_code
13:19:50:169| US
```

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13:19:50:169| u-boot>

##

<time:13:19:50> check country_code == US ok

##

2.3.4.4.2. Check Kernel Code Information

Check Kernel code Information

get system version

13:20:39:199| V1.3.4.0-ECWO7220-L-0000

13:20:39:283| ECWO7220-L-bbdcc0#

##

<time:13:20:39> check system version == V1.3.4.0-ECWO7220-L-0000 ok

##

get system altversion

13:20:39:661| V1.3.4.0-ECWO7220-L-0000

13:20:39:700| ECWO7220-L-bbdcc0#

##

<time:13:20:39> check system altversion == V1.3.4.0-ECWO7220-L-0000 ok

##

2.3.4.4.3. Check PCIe

Check PCIE interface of 2.4g and 5g RF card

ifconfig wlan0

13:20:39:772| wlan0 Link encap:Ethernet HWaddr CC:37:AB:BB:DC:C0

13:20:39:772| UP BROADCAST DEBUG RUNNING MTU:1500 Metric:1

13:20:39:772| RX packets:0 errors:0 dropped:0 overruns:0 frame:0

13:20:39:772| TX packets:33 errors:0 dropped:0 overruns:0 carrier:0

13:20:39:772| collisions:0 txqueuelen:1000

13:20:39:772| RX bytes:0 (0.0 B) TX bytes:10089 (9.8 KiB)

13:20:39:772| Interrupt:163

13:20:39:772|

13:20:39:772| ECWO7220-L-bbdcc0# ifconfig wlan1

13:20:39:859| wlan1 Link encap:Ethernet HWaddr CC:37:AB:BB:DC:D0

13:20:39:859| BROADCAST MULTICAST MTU:1500 Metric:1

13:20:39:859| RX packets:0 errors:0 dropped:0 overruns:0 frame:0

13:20:39:859| TX packets:0 errors:0 dropped:0 overruns:0 carrier:0

13:20:39:859| collisions:0 txqueuelen:1000

13:20:39:859| RX bytes:0 (0.0 B) TX bytes:0 (0.0 B)

13:20:39:859| Interrupt:169

13:20:39:859|

13:20:39:859| ECWO7220-L-bbdcc0#

****

* PCIE Check *

2.3.4.4.4. Set 2.4G WDS mode

Set 2.4G wds mode for traffic test

set management dhcp-status down

13:21:00:792| ECWO7220-L-bbdcc0# set management static-ip 192.168.1.2

13:21:00:792| set radio wlan0 status down

13:21:00:926| ECWO7220-L-bbdcc0# set radio wlan0 status down

13:21:01:094| set radio wlan1 status up

13:21:01:229| ECWO7220-L-bbdcc0# set radio wlan1 status up

13:21:01:646| set radio wlan1 channel-policy static

13:21:01:780| ECWO7220-L-bbdcc0# set radio wlan1 channel-policy static

13:21:03:718| ECWO7220-L-bbdcc0# set radio wlan1 static-channel 7

13:21:03:845| ECWO7220-L-bbdcc0# set interface wlan0wds1 radio wlan1

13:21:04:005| ECWO7220-L-bbdcc0# set interface wlan0wds1 remote-mac CC:37:AB:BB:DC:F0

13:21:06:599| ECWO7220-L-bbdcc0# set interface wlan0wds1 status up

13:21:09:264| ECWO7220-L-bbdcc0# save-running

13:21:11:931| dman: The AP startup configuration was updated successfully.

13:21:12:052| ECWO7220-L-bbdcc0#

2.3.4.4.5. 2.4G Traffic test

2.4G Traffic test via iperf

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ping 192.168.1.20 -c 2

13:21:12:108| PING 192.168.1.20 (192.168.1.20): 56 data bytes

13:21:12:108| 64 bytes from 192.168.1.20: seq=0 ttl=64 time=19.602 ms

13:21:12:119| 64 bytes from 192.168.1.20: seq=1 ttl=64 time=40.691 ms

13:21:13:159|

13:21:13:159| --- 192.168.1.20 ping statistics ---

13:21:13:159| 2 packets transmitted, 2 packets received, 0% packet loss

13:21:13:159| round-trip min/avg/max = 19.602/30.146/40.691 ms

13:21:13:159| ECWO7220-L-bbdcc0#

13:21:53:521| ECWO7220-L-bbdcc0#

##

######

<time:13:21:55> iperf -c 192.168.1.113 -f m -w 128k -P 24 -t 15 -r

--

Server listening on TCP port 5001

TCP window size: 0.13 MByte

#

--

--

Client connecting to 192.168.1.113, TCP port 5001

TCP window size: 0.13 MByte

#

--

[324] local 192.168.1.120 port 58999 connected with 192.168.1.113 port 5001

[332] local 192.168.1.120 port 59000 connected with 192.168.1.113 port 5001

[316] local 192.168.1.120 port 58998 connected with 192.168.1.113 port 5001

[292] local 192.168.1.120 port 58995 connected with 192.168.1.113 port 5001

[260] local 192.168.1.120 port 58991 connected with 192.168.1.113 port 5001

[284] local 192.168.1.120 port 58994 connected with 192.168.1.113 port 5001

[252] local 192.168.1.120 port 58990 connected with 192.168.1.113 port 5001

[228] local 192.168.1.120 port 58987 connected with 192.168.1.113 port 5001

[196] local 192.168.1.120 port 58983 connected with 192.168.1.113 port 5001

[220] local 192.168.1.120 port 58986 connected with 192.168.1.113 port 5001

[188] local 192.168.1.120 port 58982 connected with 192.168.1.113 port 5001

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[164] local 192.168.1.120 port 58979 connected with 192.168.1.113 port 5001
[156] local 192.168.1.120 port 58978 connected with 192.168.1.113 port 5001
[276] local 192.168.1.120 port 58993 connected with 192.168.1.113 port 5001
[308] local 192.168.1.120 port 58997 connected with 192.168.1.113 port 5001
[244] local 192.168.1.120 port 58989 connected with 192.168.1.113 port 5001
[212] local 192.168.1.120 port 58985 connected with 192.168.1.113 port 5001
[140] local 192.168.1.120 port 58977 connected with 192.168.1.113 port 5001
[180] local 192.168.1.120 port 58981 connected with 192.168.1.113 port 5001
[300] local 192.168.1.120 port 58996 connected with 192.168.1.113 port 5001
[268] local 192.168.1.120 port 58992 connected with 192.168.1.113 port 5001
[204] local 192.168.1.120 port 58984 connected with 192.168.1.113 port 5001
[236] local 192.168.1.120 port 58988 connected with 192.168.1.113 port 5001
[172] local 192.168.1.120 port 58980 connected with 192.168.1.113 port 5001
[ID] Interval Transfer Bandwidth
[284] 0.0-15.3 sec 3.07 MBytes 1.69 Mbits/sec
[172] 0.0-15.3 sec 2.98 MBytes 1.64 Mbits/sec
[316] 0.0-15.3 sec 2.63 MBytes 1.44 Mbits/sec
[260] 0.0-15.3 sec 3.07 MBytes 1.69 Mbits/sec
[324] 0.0-15.3 sec 3.07 MBytes 1.69 Mbits/sec
[228] 0.0-15.3 sec 3.08 MBytes 1.69 Mbits/sec
[292] 0.0-15.3 sec 3.05 MBytes 1.67 Mbits/sec
[252] 0.0-15.3 sec 3.07 MBytes 1.69 Mbits/sec
[276] 0.0-15.3 sec 3.06 MBytes 1.68 Mbits/sec
[308] 0.0-15.3 sec 3.06 MBytes 1.68 Mbits/sec
[180] 0.0-15.3 sec 3.05 MBytes 1.67 Mbits/sec
[164] 0.0-15.3 sec 2.32 MBytes 1.27 Mbits/sec
[ID] Interval Transfer Bandwidth
[188] 0.0-15.3 sec 3.06 MBytes 1.68 Mbits/sec
[268] 0.0-15.3 sec 2.24 MBytes 1.23 Mbits/sec
[196] 0.0-15.3 sec 3.07 MBytes 1.69 Mbits/sec
[300] 0.0-15.3 sec 2.39 MBytes 1.31 Mbits/sec
[ID] Interval Transfer Bandwidth
[212] 0.0-15.3 sec 3.07 MBytes 1.69 Mbits/sec
[ID] Interval Transfer Bandwidth

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[220] 0.0-15.3 sec 3.03 MBytes 1.66 Mbits/sec

[236] 0.0-15.3 sec 3.01 MBytes 1.65 Mbits/sec

[ID] Interval Transfer Bandwidth

[140] 0.0-15.3 sec 3.02 MBytes 1.66 Mbits/sec

[204] 0.0-15.3 sec 2.98 MBytes 1.64 Mbits/sec

[156] 0.0-15.3 sec 1.96 MBytes 1.08 Mbits/sec

[ID] Interval Transfer Bandwidth

[332] 0.0-15.3 sec 2.16 MBytes 1.19 Mbits/sec

[244] 0.0-15.3 sec 2.78 MBytes 1.53 Mbits/sec

[512] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2532

[472] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2533

[416] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2534

[476] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2535

[524] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2536

[ID] Interval Transfer Bandwidth

[SUM] 0.0-15.3 sec 68.3 MBytes 37.5 Mbits/sec

#

[340] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2537

[100] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2538

[96] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2539

[248] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2540

[140] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2541

[236] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2542

[216] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2543

[200] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2544

[272] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2545

[192] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2546

[308] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2547

[276] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2548

[252] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2549

[292] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2550

[228] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2551

[324] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2552

[260] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2553

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[316] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2554

[172] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2555

[ID] Interval Transfer Bandwidth

[524] 0.0-15.7 sec 5.13 MBytes 2.74 Mbits/sec

[272] 0.0-15.7 sec 5.10 MBytes 2.73 Mbits/sec

[292] 0.0-15.7 sec 5.09 MBytes 2.73 Mbits/sec

[200] 0.0-15.7 sec 5.10 MBytes 2.73 Mbits/sec

[216] 0.0-15.7 sec 5.09 MBytes 2.72 Mbits/sec

[248] 0.0-15.7 sec 4.20 MBytes 2.24 Mbits/sec

[96] 0.0-15.7 sec 5.13 MBytes 2.74 Mbits/sec

[276] 0.0-15.7 sec 5.05 MBytes 2.70 Mbits/sec

[228] 0.0-15.7 sec 5.07 MBytes 2.71 Mbits/sec

[192] 0.0-15.7 sec 5.10 MBytes 2.73 Mbits/sec

[260] 0.0-15.7 sec 5.14 MBytes 2.75 Mbits/sec

[308] 0.0-15.7 sec 5.12 MBytes 2.73 Mbits/sec

[472] 0.0-15.7 sec 5.17 MBytes 2.76 Mbits/sec

[316] 0.0-15.7 sec 5.10 MBytes 2.73 Mbits/sec

[324] 0.0-15.7 sec 5.08 MBytes 2.72 Mbits/sec

[416] 0.0-15.7 sec 5.08 MBytes 2.71 Mbits/sec

[172] 0.0-15.7 sec 5.10 MBytes 2.73 Mbits/sec

[340] 0.0-15.7 sec 5.14 MBytes 2.74 Mbits/sec

[236] 0.0-15.7 sec 4.46 MBytes 2.38 Mbits/sec

[100] 0.0-15.7 sec 2.34 MBytes 1.25 Mbits/sec

[ID] Interval Transfer Bandwidth

[140] 0.0-15.7 sec 4.88 MBytes 2.60 Mbits/sec

[252] 0.0-15.7 sec 4.72 MBytes 2.52 Mbits/sec

[476] 0.0-15.8 sec 4.72 MBytes 2.51 Mbits/sec

[512] 0.0-15.8 sec 3.98 MBytes 2.12 Mbits/sec

[SUM] 0.0-15.8 sec 116 MBytes 61.8 Mbits/sec

#

D:\test_project\OAP1122B-0216-EC\OAP1122B-0216-EC_PFT_V1.3.4.0.0Y1.00.01\extapp\Iperf>

#

#

##

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######

##

<time:13:21:55> Pass:Radio 2G tx :37.5 > 15

##

##

<time:13:21:55> Pass:Radio 2G rx :61.8 > 15

##

2.3.4.4.6. Set 5G WDS mode

Set 5G wds mode for traffic test

13:22:14:120| ECWO7220-L-bbdcc0# set management static-ip 192.168.1.3

13:22:14:120| set radio wlan1 status down

13:22:14:259| ECWO7220-L-bbdcc0# set radio wlan1 status down

13:22:14:422| set radio wlan0 status up

13:22:14:567| ECWO7220-L-bbdcc0# set radio wlan0 status up

13:22:14:916| set radio wlan0 channel-policy static

13:22:15:073| ECWO7220-L-bbdcc0# set radio wlan0 channel-policy static

13:22:16:976| ECWO7220-L-bbdcc0# set radio wlan0 static-channel 157

13:22:17:122| ECWO7220-L-bbdcc0# set interface wlan0wds0 remote-mac CC:37:AB:BB:DC:E0

13:22:17:385| ECWO7220-L-bbdcc0# set interface wlan0wds0 status up

13:22:20:067| ECWO7220-L-bbdcc0# save-running

13:22:22:733| dman: The AP startup configuration was updated successfully.

13:22:22:825| ECWO7220-L-bbdcc0#

2.3.4.4.7. 5G Traffic test

5G Traffic test via iperf

ping 192.168.1.20 -c 2

13:22:22:893| PING 192.168.1.20 (192.168.1.20): 56 data bytes

13:22:22:893| 64 bytes from 192.168.1.20: seq=0 ttl=64 time=9.375 ms

13:22:22:893| 64 bytes from 192.168.1.20: seq=1 ttl=64 time=1.359 ms

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13:22:23:908|

13:22:23:908| --- 192.168.1.20 ping statistics ---

13:22:23:908| 2 packets transmitted, 2 packets received, 0% packet loss

13:22:23:908| round-trip min/avg/max = 1.359/5.367/9.375 ms

13:22:23:908| ECWO7220-L-bbdcc0#

##

######

<time:13:23:07> iperf -c 192.168.1.113 -f m -w 128k -P 24 -t 15 -r

--

Server listening on TCP port 5001

TCP window size: 0.13 MByte

#

--

--

Client connecting to 192.168.1.113, TCP port 5001

TCP window size: 0.13 MByte

#

--

[340] local 192.168.1.120 port 59028 connected with 192.168.1.113 port 5001

[324] local 192.168.1.120 port 59026 connected with 192.168.1.113 port 5001

[332] local 192.168.1.120 port 59027 connected with 192.168.1.113 port 5001

[316] local 192.168.1.120 port 59025 connected with 192.168.1.113 port 5001

[292] local 192.168.1.120 port 59022 connected with 192.168.1.113 port 5001

[300] local 192.168.1.120 port 59023 connected with 192.168.1.113 port 5001

[268] local 192.168.1.120 port 59019 connected with 192.168.1.113 port 5001

[236] local 192.168.1.120 port 59015 connected with 192.168.1.113 port 5001

[284] local 192.168.1.120 port 59021 connected with 192.168.1.113 port 5001

[260] local 192.168.1.120 port 59018 connected with 192.168.1.113 port 5001

[252] local 192.168.1.120 port 59017 connected with 192.168.1.113 port 5001

[204] local 192.168.1.120 port 59011 connected with 192.168.1.113 port 5001

[228] local 192.168.1.120 port 59014 connected with 192.168.1.113 port 5001

[172] local 192.168.1.120 port 59007 connected with 192.168.1.113 port 5001

[220] local 192.168.1.120 port 59013 connected with 192.168.1.113 port 5001

[308] local 192.168.1.120 port 59024 connected with 192.168.1.113 port 5001

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[276] local 192.168.1.120 port 59020 connected with 192.168.1.113 port 5001
[188] local 192.168.1.120 port 59009 connected with 192.168.1.113 port 5001
[196] local 192.168.1.120 port 59010 connected with 192.168.1.113 port 5001
[164] local 192.168.1.120 port 59006 connected with 192.168.1.113 port 5001
[212] local 192.168.1.120 port 59012 connected with 192.168.1.113 port 5001
[244] local 192.168.1.120 port 59016 connected with 192.168.1.113 port 5001
[148] local 192.168.1.120 port 59005 connected with 192.168.1.113 port 5001
[180] local 192.168.1.120 port 59008 connected with 192.168.1.113 port 5001

# [ID]	Interval	Transfer	Bandwidth	#
# [340]	0.0-15.0 sec	40.3 MBytes	22.5 Mbits/sec	#
# [164]	0.0-15.0 sec	37.0 MBytes	20.7 Mbits/sec	#
# [188]	0.0-15.0 sec	39.9 MBytes	22.3 Mbits/sec	#
# [252]	0.0-15.0 sec	41.5 MBytes	23.2 Mbits/sec	#
# [300]	0.0-15.0 sec	36.5 MBytes	20.3 Mbits/sec	#
# [316]	0.0-15.0 sec	34.7 MBytes	19.4 Mbits/sec	#
# [212]	0.0-15.0 sec	41.7 MBytes	23.3 Mbits/sec	#
# [284]	0.0-15.0 sec	38.3 MBytes	21.4 Mbits/sec	#
# [172]	0.0-15.0 sec	40.5 MBytes	22.6 Mbits/sec	#
# [260]	0.0-15.0 sec	38.9 MBytes	21.7 Mbits/sec	#
# [292]	0.0-15.0 sec	39.3 MBytes	21.9 Mbits/sec	#
# [244]	0.0-15.0 sec	44.1 MBytes	24.6 Mbits/sec	#
# [324]	0.0-15.0 sec	37.0 MBytes	20.6 Mbits/sec	#
# [148]	0.0-15.0 sec	40.8 MBytes	22.7 Mbits/sec	#
# [180]	0.0-15.0 sec	41.5 MBytes	23.1 Mbits/sec	#
# [196]	0.0-15.0 sec	34.7 MBytes	19.3 Mbits/sec	#
# [204]	0.0-15.0 sec	40.1 MBytes	22.4 Mbits/sec	#
# [228]	0.0-15.0 sec	34.0 MBytes	18.9 Mbits/sec	#
# [220]	0.0-15.0 sec	39.2 MBytes	21.8 Mbits/sec	#
# [236]	0.0-15.0 sec	35.2 MBytes	19.6 Mbits/sec	#

# [ID]	Interval	Transfer	Bandwidth	#
# [308]	0.0-15.0 sec	38.8 MBytes	21.6 Mbits/sec	#
# [268]	0.0-15.0 sec	39.0 MBytes	21.7 Mbits/sec	#
# [332]	0.0-15.0 sec	37.7 MBytes	21.0 Mbits/sec	#
# [276]	0.0-15.0 sec	42.9 MBytes	23.9 Mbits/sec	#

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[SUM] 0.0-15.0 sec 933 MBytes 520 Mbits/sec

#

[108] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2558

[420] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2559

[352] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2560

[372] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2561

[412] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2562

[380] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2563

[404] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2564

[400] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2565

[388] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2566

[408] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2567

[440] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2568

[436] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2569

[356] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2570

[428] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2571

[368] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2572

[384] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2573

[396] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2574

[376] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2575

[432] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2576

[360] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2577

[364] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2578

[392] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2579

[416] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2580

[424] local 192.168.1.120 port 5001 connected with 192.168.1.113 port 2581

[ID] Interval Transfer Bandwidth

[424] 0.0-15.0 sec 36.3 MBytes 20.3 Mbits/sec

[396] 0.0-15.1 sec 33.1 MBytes 18.4 Mbits/sec

[368] 0.0-15.1 sec 37.2 MBytes 20.7 Mbits/sec

[432] 0.0-15.1 sec 31.5 MBytes 17.5 Mbits/sec

[400] 0.0-15.1 sec 37.9 MBytes 21.1 Mbits/sec

[392] 0.0-15.0 sec 39.3 MBytes 21.9 Mbits/sec

[408] 0.0-15.1 sec 37.6 MBytes 21.0 Mbits/sec

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[436] 0.0-15.1 sec 36.4 MBytes 20.3 Mbits/sec
[376] 0.0-15.1 sec 39.1 MBytes 21.8 Mbits/sec
[352] 0.0-15.1 sec 42.6 MBytes 23.7 Mbits/sec
[364] 0.0-15.1 sec 39.6 MBytes 22.1 Mbits/sec
[420] 0.0-15.1 sec 40.4 MBytes 22.5 Mbits/sec
[440] 0.0-15.1 sec 36.2 MBytes 20.2 Mbits/sec
[108] 0.0-15.1 sec 35.0 MBytes 19.5 Mbits/sec
[380] 0.0-15.1 sec 30.2 MBytes 16.8 Mbits/sec
[384] 0.0-15.1 sec 31.9 MBytes 17.8 Mbits/sec
[412] 0.0-15.1 sec 33.8 MBytes 18.8 Mbits/sec
[416] 0.0-15.1 sec 35.6 MBytes 19.8 Mbits/sec
[404] 0.0-15.1 sec 35.4 MBytes 19.7 Mbits/sec
[356] 0.0-15.1 sec 38.7 MBytes 21.5 Mbits/sec
[ID] Interval Transfer Bandwidth
[360] 0.0-15.1 sec 29.9 MBytes 16.7 Mbits/sec
[388] 0.0-15.1 sec 35.2 MBytes 19.6 Mbits/sec
[428] 0.0-15.1 sec 28.1 MBytes 15.6 Mbits/sec
[372] 0.0-15.1 sec 38.6 MBytes 21.4 Mbits/sec
[SUM] 0.0-15.1 sec 860 MBytes 478 Mbits/sec
#
D:\test_project\OAP1122B-0216-EC\OAP1122B-0216-EC_PFT_V1.3.4.0.0Y1.00.01\extapp\Iperf>
#
#
##
######

##
<time:13:23:07> Pass:Radio 5G tx :520 > 450
##

##
<time:13:23:07> Pass:Radio 5G rx :478 > 400
##

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2.3.4.5 Test Log



EC1609000280_PASS_N_1_131940.cap

2.3.4.6 Test Estimated Cycle Test

Station Name	Max of Units	Estimation (Second (s) of Time)
FT2	1	220(s)

Notice :

(1) This is not really cycle time of the DUT run, it just only references for engineering and manufacturing.

(2) The program does not comprise the factitious manipulation time in manufacturing.

(3) ACCTON IE has controlled evaluation the real testing cycle time in manufacturing.